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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/533,524	05/02/2005	Hideki Ishida	050184	1622
23850	7590	10/26/2005	EXAMINER	
ARMSTRONG, KRATZ, QUINTOS, HANSON & BROOKS, LLP			THOMAS, ERIC W	
1725 K STREET, NW			ART UNIT	
SUITE 1000			PAPER NUMBER	
WASHINGTON, DC 20006			2831	

DATE MAILED: 10/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/533,524

Applicant(s)

ISHIDA ET AL.

Examiner

Eric Thomas

Art Unit

2831

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 02 May 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-4 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-4 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 02 May 2005 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☒ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 5/05.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_.

## DETAILED ACTION

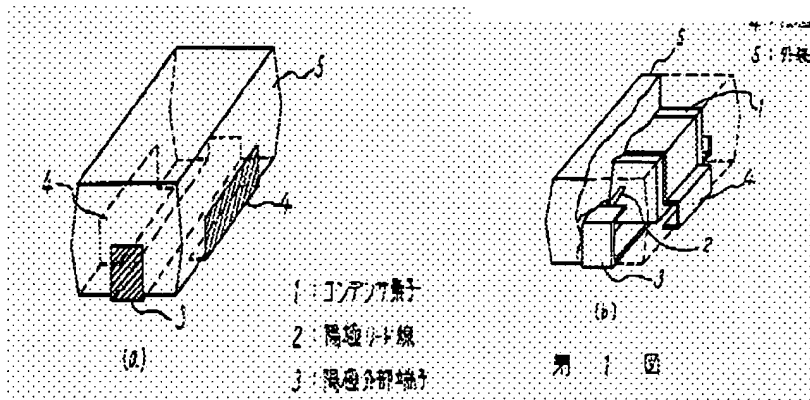
### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-3 are rejected under 35 U.S.C. 102(b) as being anticipated by JP 2-105511 ('511).



'511 discloses in fig. 1A, 1B a solid electrolytic capacitor comprising a capacitor element in which a dielectric coating layer and a cathode layer are sequentially formed on a surface of an anode element having an anode lead member planted on one end surface thereof an anode terminal connected with the anode lead member, a platy cathode terminal mounting the capacitor element thereon and connected with the cathode layer, and an enclosure resin coating the capacitor element, a part of the cathode terminal (4) and a part of the anode terminal (3) being exposed on a same plane from the enclosure resin (5), wherein the cathode terminal is provided with a

cathode exposed portion exposed from the enclosure resin in at least two locations on the same plane.

Regarding claim 2, '511 discloses the cathode exposed portion comprises a first cathode exposed portion and a second cathode exposed portion, and the first cathode exposed portion is formed in a closer location to anode exposed portion than the second cathode exposed portion is.

Regarding claim 3, '511 discloses the first cathode exposed portion extends to end portions of the solid electrolytic capacitor in a transverse direction on the same plane.

***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

5. Claims 4/1, 4/2/1, 4/3/2/1 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP2-105511 ('511) in view of JP 2001-68841 ('841).

Regarding claims 4/1, 4/2/1, 4/3/2/1, '511 discloses the claimed invention except for the mounting method for a solid electrolytic capacitor for fixing the solid electrolytic capacitor to a circuit board through a solder, wherein the circuit board has lands each provided in a position corresponding to each of the anode exposed portion and cathode exposed portion and the solder is pasted on each of the lands to solder the solid electrolytic capacitor to the circuit board.

'841 teaches a method for mounting a chip component to a printed circuit board through a solder material, wherein the circuit board has lands each provided in a position corresponding to each of the electrode exposed portions and the solder is pasted on each of the lands to solder the chip component to the circuit board.

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use the mounting method of '841 to mount the solid electrolytic capacitor of '511 to a printed circuit board, since such a modification would mount the capacitor to a printed circuit board wherein the packaging density of chip mold electronic parts can be made high.

### ***Conclusion***

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

2004/0160730 – chip-type capacitor

2002/0163775 – method of forming a chip type capacitor.


5478965 – fused chip-type solid electrolytic capacitor.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eric Thomas whose telephone number is 571-272-1985. The examiner can normally be reached on Monday - Friday 6:30 AM - 3:45 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean Reichard can be reached on 571-272-1984. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ewt

 10-18-05  
**ERIC W. THOMAS**  
**PRIMARY EXAMINER**